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NII Hiromori

IAPON

c'o NII Patent Firm

11-25, Nishinakajima 3-cheme



### From the INTERNATIONAL BUREAU

# PCT

FIRST NOTICE INFORMING THE APPLICANT OF THE COMMUNICATION OF THE INTERNATIONAL APPLICATION (TO DESIGNATED OFFICES WHICH DO NOT APPLY THE 30 MONTH TIME LIMIT UNDER ARTICLE 22(I)

#### (PCT Rule 47 1(c))

Date of mailing (duy/month/year) 03 November 2005 (03.11.2005)

Applicant's or agent's file reference

P38284-P0
International application No.
PCT/JP2005/006490

International filing date (day/month/year) 01 April 2005 (01.04.2005)

3rd Floor, Shin-Osaka Suehiro Center Bldn

Yodogawa-ku, Osaka-shi, Osaka 5320011

IMPORTANT NOTICE

Priority date (day/month/year) 01 April 2004 (01.04.2004)

Applicant

## MATSUSHITA ELECTRIC INDUSTRIAL CO , LTD. et al

- ATTENTION: For any designated Office(s), for which the time limit under Article 22(1), as in force from 1 April 2002 (30 months from the priority date), does apply, please see Form PCT/BI/308(Second and Supplementary Notice) (to be issued promptly after the expiration of 28 months from the priority date).
- Notice is hereby given that the following designated Offsec(s), for which the time limit under Article 22(1), as in force from 1. April 2002, does not apply, has/have requested that the communication of the unternational application, as provided for in Article 20, he effected under Rule 93bz. 1. The International Blueau has effected that communication on the date indicated below.

CU

In accordance with Rule 47.1(e-in/ti), those Offices will accept the present notice as conclusive evidence that the communication of the international application has duly taken place on the date of multing indicated above and no copy of the international application is required to be furnished by the applicant or the dissipated Office(s).

The following designated Offices, for which the time limit under Article 22(1), as in force from 1 April 2002, does not apply, have not requested, as at the time of mailing of the present notice, that the communication of the international application be effected under Rule 936t.11:

LU. SE, TZ, UG, ZM

In accordance with Rule 47.1(e-bi3(ii), those Offices accept the present notice as conclusive evidence that the Contracting State for which that Office arts as a designated Office does not require the fernishing, under Article 22, by the applicant of a copy of the international anolication.

4. TIME LIMITS for entry into the national phase

For the designated Office(s) listed above, and unless a demand for international preliminary examination has been filed before the expiration of 19 months from the promity that (see Article 59(1)), the applicable time limit for entering the national phase will, subject to what it said in the following paragraph, be 20 MONTHS from the priority date:

In practice, thre limits other than the 20-month time limit will continue to apply, for various periods of time, in report of certain of the designated Offices listed above. For regular updates on the applicable time limits (20 or 21 monts), or other time hint), office by Office, refer to the PCT General, the PCT Newdorm and the PCT Applicant's Guide, Volume II, National Chapters, all available from MPC9 is hintered such an Interface was monofined inclinately into

It is the applicant's sole responsibility to monitor all these time limits.

Co. . .

The International Bureau of WIPO
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211 Geneva 20, Switzerland

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## (19) 世界知的所有権機関 国際事務局



## (43) 国際公開日 2005年10月13日(13.10.2005)

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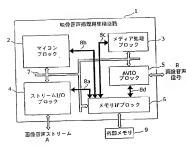
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/続菜有/

# (54) Title: INTEGRATED CIRCUIT FOR VIDEO/AUDIO PROCESSING

# (54) 発明の名称: 映像音声処理用集積回路



- 1.. INTEGRATED CIRCUIT FOR VIDEO/AUDIO PROCESSING
- 2. MICROCOMPUTER BLOCK
- 4., STREAM I/O BLOCK
- 3.. MEDIA PROCESSING BLOCK
- 5 AVIO BLOCK
- 6., MEMORY IF BLOCK
- 9.. EXTERNAL MEMORY A., VIDEO/AUDIO STREAM
- B .. VIDEO/AUDIO SIGNAL

(57) Abstract: There is provided an integrated circuit for video/audio processing capable of using a design resource obtained in a development of a video/audio device in other type of video/audio device. The integrated circuit for video/audio processing includes: a microcomputer block (2) having a CPU; a stream I/O block (4) for performing I/O of a video and audio stream with an external device; a media processing block (3) for executing media processing including at least one of compression and decompression of video and audio streams inputted to the stream I/O block (4); an AVIO block (5) for converting the video and audio stream subjected to media processing in the media processing block (3) into a video and audio signal for output to an external device or the like; and a memory IF block (6) for controlling data transfer between: the microcomputer block (2), the stream I/O block (4), the media processing block (3), and the AVIO block (5), and an external memory

(57) 要約: 映像音声機器の開発 で得られた設計資産が他の種類 の映像音声機器にも流用するこ

CPUを含むマイコンブロック2 とが可能な映像音声処理用集積回路を提供する。

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# WO 2005/096168 A1

- (84) 指定国 (表示のない限り、全ての種類の広域保護 が可能: ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), ユーラシア (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), ヨーロッパ (AT, BE,

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## 添付公開書類:

## - 国際調査報告銀

請求の範囲の補正の期限前の公開であり、補正書受 領の際には再公開される。

2文字コード及び他の略語については、定期発行される各PCTガゼットの巻頭に掲載されている「コードと略語のガイダンスノート」を参照。